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1000 U-972953  
10/10/01

	Subclass	ISSUE CLASSIFICATION
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PATENT NUMBER

## **U.S. UTILITY PATENT APPLICATION**

O.I.P.E.

**PATENT DATE**

KG SCANNED 74BZ Q.A. 16

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/972953	F	716 427	51	2825-2	Talbot

## APPLICANTS

Han-Chieh Chang  
Chang-Ming Chen  
Hsuan Peng  
Min-Wen Wang

TITLE

**Method and apparatus for the formation of laminated circuit having passive components therein**

PTO-2040  
12/99

**PREPARED AND APPROVED FOR ISSUE**

## **ISSUING CLASSIFICATION**

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> a) The term of this patent subsequent to _____ (date) has been disclaimed.				<b>NOTICE OF ALLOWANCE MAILED</b>	
	(Assistant Examiner)	(Date)			
<input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ .  _____				<b>ISSUE FEE</b>	
	(Primary Examiner)	(Date)		Amount Due	Date Paid
<input type="checkbox"/> c) The terminal ____months of this patent have been disclaimed.				<b>ISSUE BATCH NUMBER</b>	
	(Legal Instruments Examiner)	(Date)			

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(Rev. 10/97)

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